



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No.	10/767,751	Confirmation No.	5740
First Inventor	Richard K. Williams	Filing Date	01/28/2004
Tech. Center/ Art Unit	2812	Examiner	Matthew E. Warren
Title:	Modular Bipolar-CMOS-DMOS Analog Integrated Circuit And Power Technology		
Docket No.:	AAT007-4C US	Customer No.:	34036

Santa Clara, California
May 25, 2006

Mail Stop Issue Fee
COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO NOTICE REGARDING DRAWINGS

Dear Sir:

In response to the Notice Regarding Drawings dated May 9, 2006, please amend the above-identified application as follows.

Amendments to the Specification begin on page 2 of this paper.

Remarks/Arguments begin on page 3 of this paper.